

[*MULTI-INTERPOLATED DATA RECOVERY WITH A RELATIVE LOW SAMPLING RATE*]

Abstract of Disclosure

A method and related circuit for recovering a digital signal can operate with a sampling frequency less than a data frequency. The circuit includes a plurality of interpolators for generating an output signal. The output signal is used by a computation module to generate a plurality of control words. The control words are used by the interpolators to perform weighted interpolation functions to precisely generate the output signal. A data circuit can then extract the original input signal digital information from the output signal.

Figures